

Case Study: Performance Results of Avaliant Mercury

The goal of this case study is to compare the performance results of Avaliant Mercury to three known LDPC code standards, namely: 802.11ad, 802.16, and DVB-S2.

802.11ad LDPC Codes

The IEEE 802.11ad is the wireless standard protocol that was developed by the Wireless Gigabit Alliance (WiGig) to operate in the unlicensed 60 GHz frequency band. The 802.11ad standard can achieve a theoretical maximum data rate of 7 Gbps [1]. The 802.11ad LDPC code rates include ½, 5/8, ¾, and 13/16 with a common block length of 672 bits. The 802.11ad LDPC codes are irregular. Appendix A shows the parity check matrix, H, for the various code rates. The published Bit Error Rate (BER) performance results for the 802.11ad LDPC codes with QPSK, 16QAM and 64QAM modulation are given in [2, Figure 5a]. Figure 1, Figure 2, Figure 3, and Figure 4 illustrate the 802.11ad LDPC BER performance for code rates of ½, 5/8, ¾, and 13/16, respectively, for the various modulation schemes. For the Avaliant Mercury, the number of iterations is set to 30 (Reference [2] used 30 iterations for the published results), and 500 block errors are accumulated. It is show that the Avaliant Mercury results match the published results for all modulation formats and coding rates. The BER curves using Avaliant Mercury were simulated down to 10⁻⁷ BER to detect the presence of error floor or lack thereof.

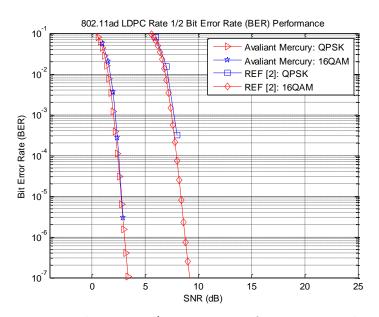


Figure 1: 802.11ad LDPC Rate=1/2 Bit Error Rate Performance, QPSK and 16QAM



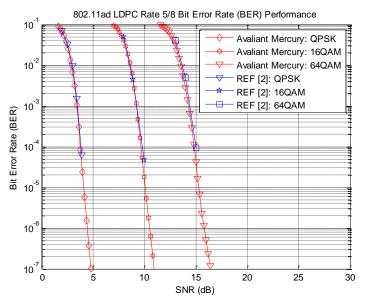


Figure 2: 802.11ad LDPC Rate=5/8 Bit Error Rate Performance, QPSK, 16QAM and 64QAM

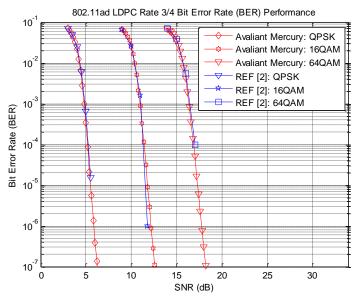


Figure 3: 802.11ad LDPC Rate=3/4 Bit Error Rate Performance, QPSK, 16QAM and 64QAM



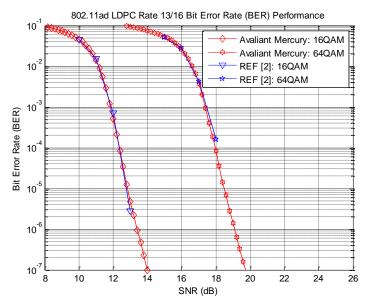


Figure 4: 802.11ad LDPC Rate=13/16 Bit Error Rate Performance, 16QAM and 64QAM

802.16 LDPC Codes

The IEEE 802.16 is the wireless standard that is commercially known as Worldwide Interoperability for Microwave Access (WiMAX). WiMAX goal is to provide the "last mile" broadband access in metropolitan area network (MAN) [3]. The 802.16 LDPC code rates include ½, 2/3 A, 2/3 B, ¾ A, ¾ B, and 5/6. The 802.16 LDPC codes are also irregular. Appendix B illustrates the 802.16 parity check matrix, H, for select code rates. The published BPSK performance results for the 802.16 can be found in [4, Figure 5.1 for BER and Figure 5.2 for Block Error Rate (BLER)]. Moreover, published performance results with QPSK and 16QAM modulation can be found in [5, Figure 3] for LDPC code rate of ½.

Figure 5 and Figure 6 illustrate the BER and BLER performance with BPSK modulation for various LDPC code rates. For the Avaliant Mercury, the number of iteration is set to 30 consistent with [4], and 500 block errors are accumulated. It is show that the Avaliant Mercury results match the published results. Figure 7, on the other hand, illustrates the BER performance for 802.16 rate=1/2 LDPC code with QPSK and 16QAM modulation. For the Avaliant Mercury, the number of iteration is set to 100 consistent with [5], and 500 block errors are accumulated. It is show that the Avaliant Mercury results match the published results in [5].



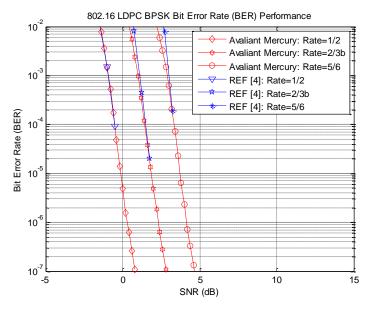


Figure 5: 802.16 BPSK LDPC Rate=1/2, 2/3b, and 5/6 BER Performance

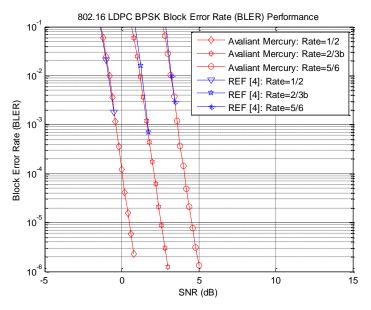


Figure 6: 802.16 BPSK LDPC Rate=1/2, 2/3b, and 5/6 BLER Performance



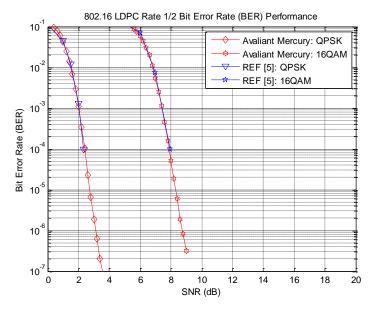


Figure 7: 802.16 LDPC Rate=1/2 BER Performance for QPSK and 16QAM

DVB-S2 LDPC Codes

The DVB-S2 [6] is a digital broadcast satellite transmission standard originally developed in 1993 by the DVB Project. DVB-S2 uses a powerful Forward Error Correcting (FEC) codes based on LDPC codes concatenated with BCH codes. The published QPSK performance results for the DVB-S2 can be found in [7, Figure A.4]. The DVB-S2 code rates investigated here include ½, 3/4, 5/6, and 9/10. For the results shown in [7], each LDPC frame is divided to multiple MPEG packets, 188 bytes each, and outer BCH code was also employed. Figure 8 shows the Packet Error Rate (PER) performance of various code rates with QPSK modulation for the normal frame of 64800 bits. Note the Avaliant Mercury performance results only include the LDPC code without the outer BCH code, assuming 50 iterations. As a result, the Avaliant Mercury performance results show a tail at low PER because the outer BCH code was not employed. The outer BCH code helps clean up additional errors at the output of the LDPC decoder and will improve the overall performance, mainly reducing an error floor that shows up at low error rate [8]. Otherwise, the results show good match.



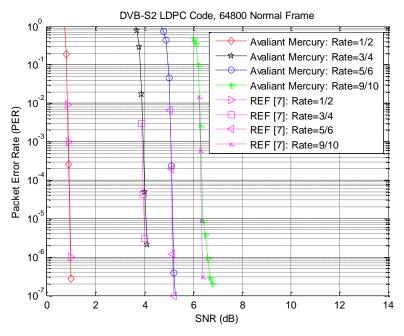


Figure 8: DVB-S2 LDPC Performance with Code Rate $\frac{1}{2}$, $\frac{3}{4}$, $\frac{5}{6}$, and $\frac{9}{10}$ with QPSK Modulation

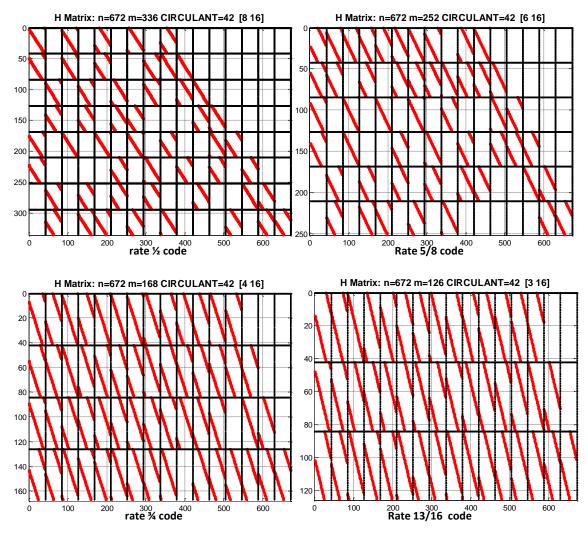


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- [2] C. Zhang et al; "Performance analysis on the OFDM PHY of IEEE 802. 11ad standard," in proc. International Conference on Computational Problem-Solving, pp. 708-713, 21-23 Oct. 2011.
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- [4]http://wvuscholar.wvu.edu/reports/Gerken Tim.pdf
- [5] <u>Baumgartner</u>, <u>B.</u>, et al. "Performance of forward error correction for IEEE 802.16 e." <u>10th</u> International OFDM Workshop (InOWo). 2005.
- [6] https://www.dvb.org/standards/dvb-s2
- [7] https://www.dvb.org/resources/public/standards/a171-1 s2 guide.pdf
- [8] https://pdfs.semanticscholar.org/a84d/5629ccf3cb812f7754a9803efcfc53ee8496.pdf



Appendix A: 802.11ad

The parity-check matrix, H, is a matrix where *n* is the length of the code, and *m* is the number of parity bits. The length of the code, n, is 672. The number of parity bits, m, is a function of the code rate. The IEEE 802.11ad LDPC codes are quasi-cyclic with sub-matrices dimension of 42x42. The 802.11ad LDPC codes are irregular. Appendix A Figure 1 shows the H matrix for the four code rates.

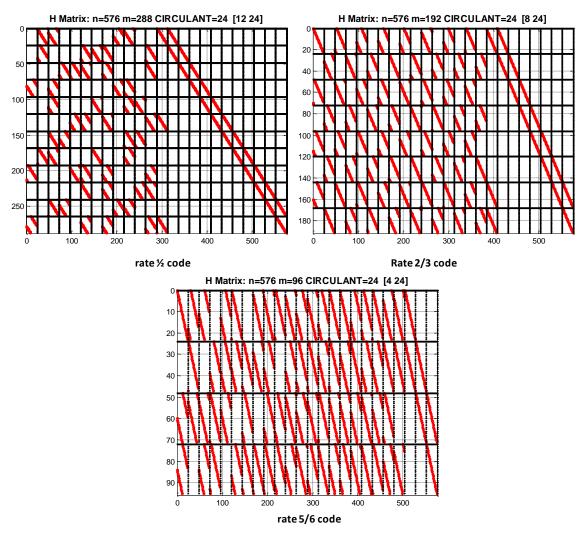


Appendix A Figure 1: Parity Check Matrix for 802.11ad rate=1/2, 5/8, ¾, and 13/16 LDPC codes.



Appendix B: 802.16

The parity-check matrix, H, is a matrix where n is the length of the code, and m is the number of parity bits. The length of the code, n, is 576. The number of parity bits, m, is a function of the code rate. The IEEE 802.16 LDPC codes are quasi-cyclic with sub-matrices dimension of 24x24. The 802.16 LDPC codes are also irregular. Appendix B Figure 1 shows the H matrix for a select code rates.



Appendix B Figure 1: Parity Check Matrix for 802.16 rate=1/2, 2/3, 5/6 LDPC codes.